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	•					
Pleas	e record the attached original documents or co	pies thereof.				
1.	Name of conveying party(ies): AGILENT TECHNOLOGIES JAPAN, I					
2.	Name and address of receiving party(ies): AGILENT TECHNOLOGIES, INC. Legal Department, DL429 Intellectual Property Administration P.O. Box 7599 Loveland, Colorado 80537-0599	FAX COPY RECEIVED MAY 1 4 2002. TECHNOLOGY CENTER 2800				
3.	Nature of conveyance: XXXXX	Merger Change of Name				
4 .	Application number(s) or application date(s): If this document is being filed together with a	new application, the execution date of				
the ap	oplication is:	•				
·	A. <u>Application/Patent No.</u> B. 10/054,139	Application Date January 22, 2002				
5.	Name and address of party to whom correspond	ondence concerning this document				
should be mailed:						
Paul D. Greeley, Esq.						
Ohlandt, Greeley, Ruggiero & Perle, L.L.P. One Landmark Square, 10 th Floor						
Stamford, Connecticut 06901-2682 U.S.A.						
6.	Total No. of applications and patents involved	l: <u>1</u> .				
7.	Total Fee (37 C.F.R. 3.41(h)) \$40.00/assignm	nent.				
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ASSIGNMENT

WHEREAS, AGILENT TECHNOLOGIES JAPAN, LTD. (a Corporation of Japan, having its principal place of business in Tokyo, Japan) has been assigned the invention(s) disclosed in the patent application entitled:

APPARATUS FOR TESTING INTEGRATED CIRCUITS

developed by: Hiroshi Sakayori and Takanori Komuro and first filed before the Japanese Patent Office on 30 March 2001. as Patent Application No. 2001-98038 whereby title to the invention(s) has vested in AGILENT TECHNOLOGIES JAPAN. LTD.

NOW, THEREFORE in accordance with an Agreement between AGILENT TECHNOLOGIES JAPAN, LTD. and AGILENT TECHNOLOGIES, INC. (a Delaware corporation having its principal place of business in Palo Alto, California, United States of America), AGILENT TECHNOLOGIES JAPAN, LTD. hereby, in all countries other than Japan, assigns to AGILENT TECHNOLOGIES, INC., its successors and assigns, the entire right, title, and interest in the invention(s) set forth in the above-identified application and the right to file and prosecute application(s) for Letters Patent in each of these countries, and hereby assigns all right, title and interest in any and all Letters Patent of these countries which may be granted thereon, including, in particular, the Letters Patent for United States Patent Application Serial No. 10/054,139 filed on January 22, 2002 entitled:

APPARATUS FOR TESTING INTEGRATED CIRCUITS

executed by Hiroshi Sakayori	~~~	March	-	
executed by Takanori Komuro	on_	March	1,	2002

IN WITNESS WHEREOF, this Assignment is executed by a duly authorized representative of AGILENT TECHNOLOGIES JAPAN, LTD.

Signature

Yoh Narimatsu

Printed Name

President

Title

Date: Mar 4 2002

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MAY 1 4 2002

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